

RESISTO Plenary Technical Meeting 16-17 of July at Altice Lab

AGENDA

Tuesday, 16th July

- 8:30 – arriving at Altice Lab Meeting room
- 9:00 – LDO project coordinator introduction
- 9:15 – Alberto NERI leads RESISTO platform/architecture plenary session – walkthrough RESISTO platform/architecture with a demo scenario.
 - Quick general architecture and main functional chain presentation,
 - Long Term Control Loop presentation by Mirjam
 - Preliminary activities needed for Short Term Control Loop setup:
 - Correlator rules definition and configuration,
 - Risk Predictor modelling activities (by Cosimo Palazzo RM3),
 - Workflows definition and configuration.
 - Short Term runtime presentation based on a case study
- 12:30 – lunch (approx. 1 hour, time to be confirmed)
- 13:30 – T2.5 societal (and ethical) impact of the project (S. Bach)
- 14:00 – D2.8 status (before this meeting) (M. Belesioti)
- 14:30 – storytelling of the use cases (1 hour for each TELCO Operator) (see WARNING at next page)
 - 14:30 – ORO
 - 15:30 – OTE
 - 16:30 – ALB
- 17:30 – finish

Evening meal (to be confirmed)

Wednesday 17th July

- 09:00 – storytelling of the use cases (1 hour for each TELCO Operator) (see WARNING at next page)
 - 09:00 – BTC
 - 10:00 – TIM
 - 11:00 – RTV
- 12:00 – testbeds; interfaces; modelling; algorithms/tools; deliverable focused topics; etc. (discussion)
- 12:30 – lunch (approx. 1 hour, time to be confirmed)
- 13:45 – D2.1 requirements tracking (S. Bach)

- 14:15 – point raised in Brussels (M. Fehling-Kaschek)
 - Security vs. resilience
 - STCL and LTCL alignment
- 15:00 – D2.7 status (before/after this meeting) (A. Neri)
- 15:30 – D2.8 status (after this meeting) (M. Belesioti)
- 16:00 – finish